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2-19-04

PATENT



S/N 09/637532

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Dmitriy Rumynin et al.	Examiner:	David H. Malzahn
Serial No.:	09/637532	Group Art Unit:	2124
Filed:	August 11, 2000	Docket:	1365.033US1
Title:	A PARALLEL COUNTER AND MULTIPLICATION LOGIC CIRCUIT		

INFORMATION DISCLOSURE STATEMENT

RECEIVED

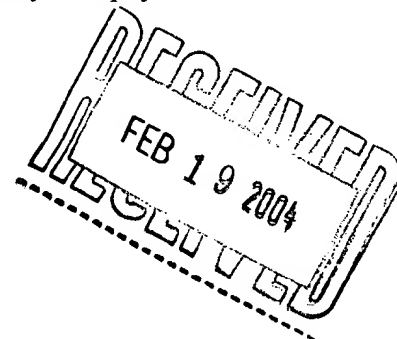
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

FEB 13 2004

Technology Center 2100

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.



02/13/2004 DTESSEM1 00000113 190743 09637532
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The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

DMITRIY RUMYNIN ET AL.

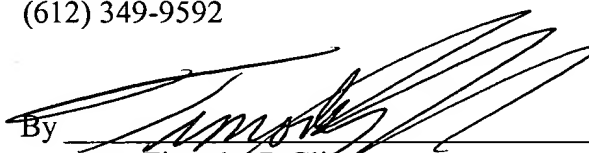
By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 349-9592

Date

9 Feb '04

By

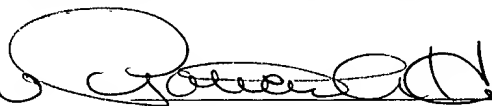

Atty: Timothy B Clise
Reg. No. 40,957

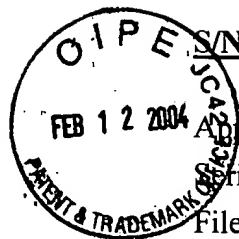
CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS: RCE; Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 9th day of February, 2004.

Name

Patricia A. D. Hman

Signature





S/N 09/637532

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Applicant: Dmitriy Rumynin et al.

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Serial No.: 09/637532

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Filed: August 11, 2000

Docket: 1365.033US1

Title: A PARALLEL COUNTER AND MULTIPLICATION LOGIC CIRCUIT

COMMUNICATION CONCERNING RELATED APPLICATION(S)**RECEIVED**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

FEB 13 2004

Technology Center 2100

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/769954	January 25, 2001	1365.039US1	A PARALLEL COUNTER AND A MULTIPLICATION LOGIC CIRCUIT
09/898752	July 3, 2001	1365.048US1	MULTIPLICATION LOGIC CIRCUIT
09/917257	July 27, 2001	1365.051US1	A PARALLEL COUNTER AND A LOGIC CIRCUIT FOR PERFORMING MULTIPLICATION
10/472658	September 22, 2003	1365.072US1	A MULTIPLICATION LOGIC CIRCUIT

Respectfully submitted,

DMITRIY RUMYNIN ET AL.

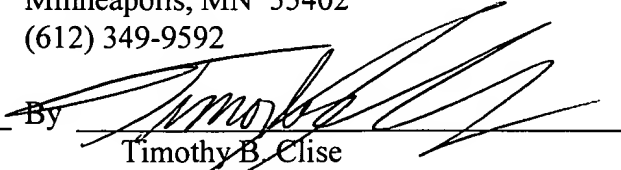
By Applicants' Representatives,

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Name

Patricia A. Holtman

Signature

